

CLAIMS

What is claimed is:

1. A method comprising:  
transmitting during an awake mode one or more data packets  
sent for transmission during a power save mode.
2. The method of claim 1, further comprising buffering said one or more data packets during said power save mode.
3. The method of claim 2, wherein transmitting during an awake mode comprises transmitting said one or more packets in response to a wake-up trigger.
4. The method of claim 3, wherein said wake-up trigger relates to an aggregate anticipated transmission time of the one or more data packets.
5. The method of claim 3, wherein said wake-up trigger relates to an aggregate size of the one or more data packets.
6. The method of claim 3, wherein said wake-up trigger relates to a period of time during which no data packets are sent for transmission.
7. The method of claim 2, wherein buffering comprises buffering one or more of said data packets based on a priority criterion.
8. The method of claim 7, wherein transmitting comprising transmitting said one or more data packets based on said priority criterion.
9. The method of claim 8, wherein said priority criterion relates to the priority of said one or more data packets.
10. The method of claim 1, wherein transmitting during an awake mode comprises transmitting an awake mode signal to indicate a start of said awake mode.

11. The method of claim 1, wherein transmitting during an awake mode comprises transmitting a power save signal to indicate an end of said awake mode.

12. The method of claim 1, comprising disabling a transmitter during said power save mode.

13. A program storage device having instructions readable by a machine that when executed by the machine result in:

transmitting during an awake mode one or more data packets sent for transmission during a power save mode.

14. The program storage device of claim 13, wherein said instructions further result in buffering said one or more data packets during said power save mode.

15. The program storage device of claim 14, wherein the instructions that result in transmitting during an awake mode comprise instructions that result in transmitting said one or more packets in response to a wake-up trigger.

16. The program storage device of claim 15, wherein said wake-up trigger relates to an aggregate anticipated transmission time of the one or more data packets.

17. The program storage device of claim 15, wherein said wake-up trigger relates to an aggregate size of the one or more data packets.

18. The program storage device of claim 15, wherein said wake-up trigger relates to a period of time during which no data packets are sent for transmission.

19. The program storage device of claim 14, wherein the instructions that result in buffering comprise instructions that result in buffering one or more of said data packets based on a priority criterion.

20. The program storage device of claim 19, wherein the instructions that result in transmitting during an awake mode comprise instructions

that result in transmitting said one or more data packets based on said priority criterion.

21. The program storage device of claim 20, wherein said priority criterion relates to the priority of said one or more data packets.

22. The program storage device of claim 13, wherein the instructions that result in transmitting during an awake mode comprise instructions that result in transmitting an awake mode signal to indicate a start of said awake mode.

23. The program storage device of claim 13, wherein the instructions that result in transmitting during an awake mode comprise instructions that result in transmitting a power save signal to indicate an end of said awake mode.

24. The program storage device of claim 13, wherein the instructions result in disabling a transmitter during said power save mode.

25. An apparatus comprising a buffer to store one or more data packets during a power save mode and to transmit said one or more data packets during an awake mode.

26. The apparatus of claim 25, further comprising a processor adapted to transmit an awake signal to indicate a start of said awake mode.

27. The apparatus of claim 26, wherein said processor is further adapted to transmit a power save signal to indicate an end of said awake mode.

28. The apparatus of claim 27, comprising a disabling unit to disable said transmitter during said power save mode.

29. The apparatus of claim 28, wherein said disabling unit is able to enable said transmitter during said power save mode.

30. A wireless communication device comprising:

a buffer to store one or more data packets during a power save mode;

a transmitter adapted to transmit said at least one data packet during an awake mode; and

an omni-directional antenna operationally coupled to said transceiver.

31. The wireless communication device of claim 30, further comprising a processor to produce said one or more data packets.

32. The wireless communication device of claim 31, wherein said transmitter is further adapted to transmit an awake mode signal to indicate a start of said awake mode.

33. The wireless communication device of claim 31, wherein said transmitter is further adapted to transmit a power save mode signal to indicate an end of said awake mode.

34. The wireless communication device of claim 31, further comprising a power source and circuitry to connect said transmitter to said power source during said awake mode.

35. The wireless communication device of claim 35, further comprising circuitry to disconnect said transmitter from said power source during a power save mode.

36. A wireless communication system comprising:

a first wireless device adapted to transmit during an awake mode one or more data packets sent for transmission during a power save mode; and

a second wireless device adapted to receive said one or more data packets.

37. The wireless communication system of claim 36, wherein said second wireless device is further adapted to transmit during said awake mode one or more data packets sent for transmission during said power save mode.

38. The wireless communication system of claim 37, wherein said first wireless device is further adapted to transmit an awake mode signal to indicate a start of said awake mode.

39. The wireless communication system of claim 38, wherein said first wireless device is further adapted to transmit a power wave mode signal to indicate an end of said awake mode.